

MD1S THRU MD7S

MINIATURE GLASS PASSIVATED SINGLE-PHASE SURFACE MOUNT BRIDGE RECTIFIER

GROWCHILD
ELECTRONICS™

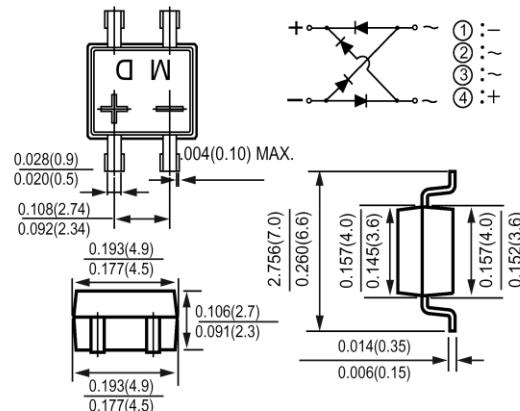
REVERSE VOLTAGE: 50 to 1000 VOLTS
FORWARD CURRENT: 0.5 AMPERE

<http://www.njzrg.com>

FEATURES

- Glass passivated chip junction
- Low forward voltage drop
- High surge overload rating of 30 Amperes peak
- Ideal for printed circuit board
- High temperature soldering guaranteed:
260°C for 10 seconds

MD-S



MECHANICAL DATA

Case: Molded plastic, MD-S

Epoxy: UL 94V-O rate flame retardant

Terminals: Leads solderable per MIL-STD-202,
method 208 guaranteed

Mounting position: Any

Weight: 0.008ounce, 0.22gram

Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Ratings at 25° ambient temperature unless otherwise specified.

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

| | Symbols | MD1S | MD2S | MD3S | MD4S | MD5S | MD6S | MD7S | Units |
|--|-----------------------------------|------|------|------|-------------|------|------|------|-------|
| Maximum Recurrent Peak Reverse Voltage | V _{RRM} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | Volts |
| Maximum RMS Voltage | V _{RMS} | 35 | 70 | 140 | 280 | 420 | 560 | 700 | Volts |
| Maximum DC Blocking Voltage | V _{DC} | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | Volts |
| Maximum Average Forward Rectified Current (see Fig. 1) on glass-epoxy P.C.B (Note 2) on aluminum substrate (Note 3) | I _(AV) | | | | 0.5 | | | | Amp |
| | | | | | 0.8 | | | | |
| Peak Forward Surge Current, 8.3ms single half-sine-wave superimposed on rated load (JEDEC method) | I _{FSM} | | | | 30 | | | | Amp |
| Maximum Forward Voltage at 0.4A DC and 25° | V _F | | | | 1.0 | | | | Volts |
| Maximum Reverse Current at T _A =25° at Rated DC Blocking Voltage T _A =125° | I _R | | | | 5.0 | | | | uAmp |
| | | | | | 500 | | | | |
| Typical Junction Capacitance (Note 1) | C _J | | | | 13 | | | | pF |
| Typical Thermal Resistance (Note 3) | R _{0JA} | | | | 70 | | | | /W |
| Typical Thermal Resistance (Note 2) | R _{0JL} | | | | 20 | | | | /W |
| Operating and Storage Temperature Range | T _J , T _{stg} | | | | -55 to +150 | | | | |

NOTES:

1- Measured at 1 MHz and applied reverse voltage of 4.0 VDC.

2- On glass epoxy P.C.B. mounted on 0.05 x 0.05" (1.3 x 1.3mm) pads

3- On aluminum substrate P.C.B. with an area of 0.8" x 0.8" (20 x 20mm) mounted on 0.05 x 0.05" (1.3 x 1.3mm) solder pad

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RATINGS AND CHARACTERISTIC CURVES

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Fig. 1 - Derating Curve for Output Rectified Current

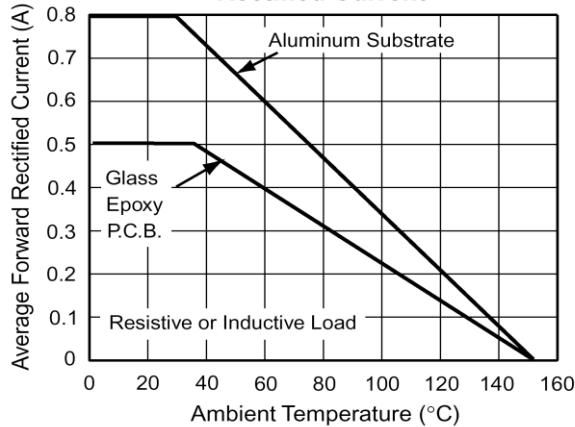


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Leg

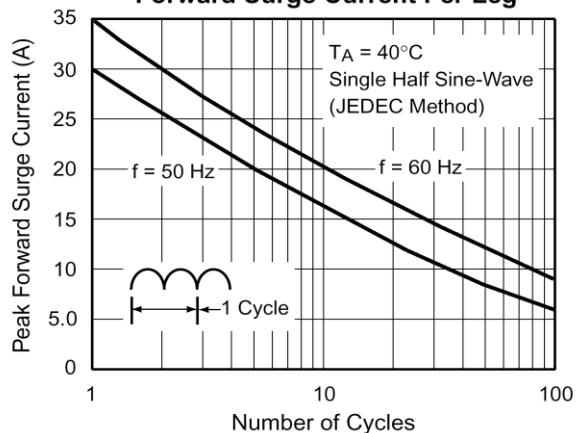


Fig. 3 - Typical Forward Voltage Characteristics Per Leg

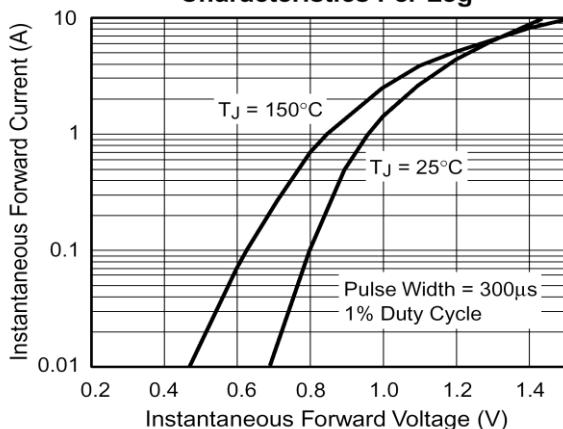


Fig. 4 - Typical Reverse Leakage Characteristics Per Leg

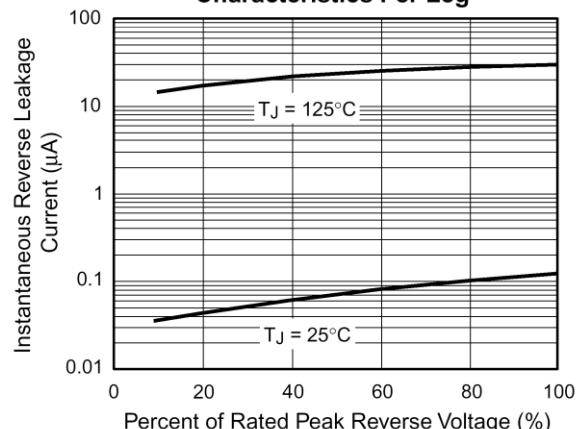


Fig. 5 - Typical Junction Capacitance Per Leg

